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<u>L25</u>	110 and 111	54	<u>L25</u>
<u>L24</u>	17 and 113	264	<u>L24</u>
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<u>L12</u>	(713/401-601)[CCLS]	4177	<u>L12</u>
<u>L11</u>	(712/2-300)[CCLS]	13995	<u>L11</u>
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<u>L10</u>	L7 and token\$1	89	<u>L10</u>
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<u>L3</u>	(instruction\$1) nea15 (clock\$1 or cycle\$1 or period\$1) near15 (delay\$5 or wait\$5) near35 (number\$1 or second or first or count\$3) near45 branch\$5	895752	<u>L3</u>
<u>L2</u>	(instruction\$1) nea15 (clock\$1 or cycle\$1 or period\$1) near15 (delay\$5 or wait\$5) near35 (number\$1 or second or first or count\$3)	962480	<u>L2</u>
<u>L1</u>	(instruction\$1) nea15 (clock\$1 or cycle\$1 or period\$1) near15 (delay\$5 or wait\$5) near35 (number\$1 or second or first)	955054	<u>L1</u>

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Incorporating speculative execution into scheduling of control-flow-intensive designs

Lakshminarayana, G.; Raghunathan, A.; Jha, N.K.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on

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